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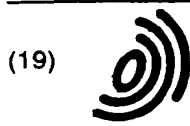
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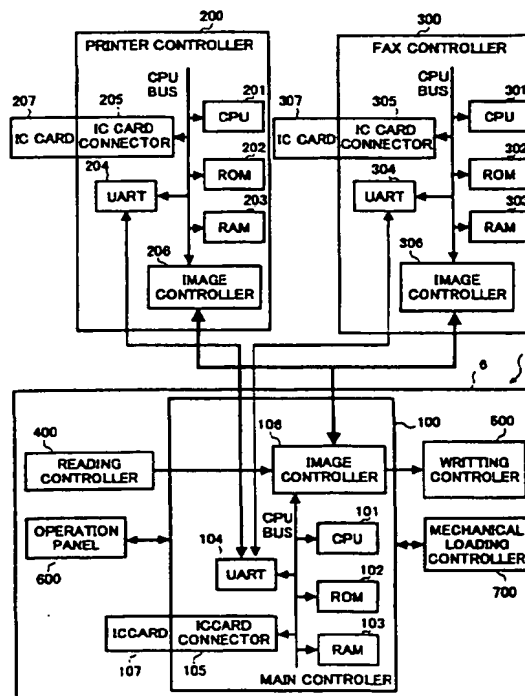
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(54) **Method and apparatus for multi-function processing capable of performing a program downloading using a common single connection, and a medium storing the method**

(57) A multi-function apparatus includes a plurality of image forming apparatuses, a buffer memory, a data transmission mechanism, and a connector. Each image forming apparatus includes a controller which includes a program memory for storing a program used by the image forming apparatus. The data transmission mechanism connects the image forming apparatuses each other so as to allow a data transmission therebetween. The connector connects an external storage to the data transmission mechanism so as to allow a data transmission from the external storage to the buffer memory. The external storage stores data of programs to be respectively used by the image forming apparatuses.

FIG. 1



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Description

[0001] The present application relates to a method and apparatus for multi-function processing, and more particularly to a method and apparatus for multi-function processing capable of performing a program downloading using a common single connection. The present application further relates to a computer readable medium for storing the above-mentioned method.

[0002] An image forming apparatus, such as a copying machine, a facsimile machine, a printer, a scanner, an electronic filing apparatus, etc., or a combination apparatus of these machines, performs an image processing operation according to a program provided to a control unit of the apparatus.

[0003] Since the above program is typically stored in an EPROM (erasable programmable read only memory) provided to the apparatus, it is needed to exchange the EPROM each time the program is changed.

[0004] In order to reduce time required for the exchange of the EPROM, programs are recently stored in a flash ROM which can be updated by a downloading of a new program from an external storage including an IC (integrated circuit) card. Thereby, the time for exchanging the program in the apparatus is dramatically shortened. In this method, the IC card, for example, is substituted for the flash ROM, that is, addresses of the flash ROM and the IC card is exchanged on a memory map, and therefore the apparatus is started with the program stored in the IC card. Thereby, the apparatus can download a new program stored in the IC card to the flash ROM.

[0005] This download method is significant especially for a recent digital multi-function apparatus which is mostly based on a digital copying machine and has a plurality of components for image forming, such as a printer, a facsimile, a scanner, etc., wherein each of the components is provided with a dedicated CPU and ROMs.

[0006] One example of the above background digital multi-function apparatus is explained with reference to Figs. 1 and 2. Fig. 1 shows a digital multi-function apparatus 5 based on a digital copying apparatus 6 having a main controller 100 and which has optional devices of a printer controller 200 and a facsimile controller 300. In this background digital multi-function apparatus 5, a CPU (central processing unit) 101 included in the main controller 100 controls the entire operations.

[0007] The background digital multi-function apparatus 5 performs a copying operation in which image data output from a reading controller 400 after an original is read by a reading unit (not shown) is sent to a writing controller 500 via an image controller 106. The writing controller 500 controls an LD (laser diode), not shown, to turn on and off according to the image data and forms an image on a transfer sheet based on a known electrophotographic procedure (not shown).

[0008] Each of the printer controller 200 and the facsimile controller 300 is connected to the digital copying apparatus 6 with a UART (Universal Asynchronous Receiver Transmitter) serving as a data transmitting configuration and an application image data bus serving as a data bus for sending data of an application image.

[0009] During a print operation, the printer controller 200 receives print data sent through a network with a network interface mechanism (not shown) and sent a print image generated by an image controller 206 based on the print data to the digital copying apparatus 6 so as to perform an image forming operation. This operation is performed by an execution of a printer application program by a CPU (central processing unit) 201 with an assistance of a ROM (read only memory) 202 and a RAM (random access memory) 203, included in the printer controller 200.

[0010] During a facsimile operation, the facsimile controller 300 transmits a facsimile image, generated and sent to the facsimile controller 300 by the digital copying apparatus 6, to a facsimile line with a facsimile line interface (not shown), or receives a facsimile signal from the facsimile line via the facsimile line interface and sends it to the digital copying apparatus 6 so as to perform the image forming operation. This operation is performed by an execution of a facsimile application program by a CPU (central processing unit) 301 with an assistance of a ROM (read only memory) 302 and a RAM (random access memory) 303, included in the facsimile controller 300.

[0011] In the above operations, the digital copying apparatus 6 exchanges with the printer controller 200 and the facsimile controller 300 control data through UARTs (Universal Asynchronous Receiver Transmitter) 104, 204, and 304 and image data through the application image data bus connecting image controllers 106, 206, and 306 each other so that the background digital multi-function apparatus 5 can perform a sequential operation under a multi-function operating system.

[0012] The main controller 100 is provided with a ROM (read only memory) 102 that stores a plurality of programs including a program used by the CPU 101 which governs the entire operations of the background digital multi-function apparatus 5. To update the programs, as described above, an IC (integrated circuit) card 107, serving as an external storage device and which stores a latest program, is connected to an IC (integrated circuit) card connector 105 and the program is downloaded to the ROM 102 via a CPU bus.

[0013] Fig. 2 shows a procedure of the program downloading performed by the background digital multi-function apparatus 5. After the IC card 107 is inserted into the IC card connector 105 in Step S701, the background digital multi-function apparatus 5 is given power in Step S702 and then a download program stored in the IC card 107 is initiated in Step S703. Thereby, the downloading program is started in Step S704.

[0014] In Step S704, the contents of the IC card 107 is sequentially transmitted to the ROM 102 via the CPU bus. Whether the transmission of the program is completed is checked in Step S706. If the transmission is determined as completed in Step S706, a process for completing the program downloading is performed in Step S707. Then, the process ends.

5 [0015] Likewise, the printer controller 200 performs an application operation according to the program stored in the ROM 202. To update the program, IC (integrated circuit) card 207 is connected to IC (integrated circuit) card connector 205 and the program stored in the IC card 207 is downloaded to the ROM 202. Also, the facsimile controller 300 performs an application operation according to the program stored in the ROM 302. To update the program, IC (integrated circuit) card 307 is connected to IC (integrated circuit) card connector 305 and the program stored in the IC
10 card 307 is downloaded to the ROM 302.

[0016] However, the background digital multi-function apparatus 5 needs to have more than one mechanism for receiving the IC card and transmitting the contents of the IC card. This results in an increase of the manufacturing cost.

[0017] The present application describes a novel multi-function apparatus. In one example, a novel multi-function apparatus includes a plurality of image forming apparatuses, a buffer memory, a data transmission mechanism, and
15 a connector. Each of the image forming apparatuses includes a controller which includes a program memory for storing a program used by the image forming apparatus. The data transmission mechanism is configured to connect the plurality of image forming apparatuses each other so as to allow a data transmission therebetween. The connector is configured to connect an external storage to the data transmission mechanism so as to allow a data transmission from the external storage to the buffer memory. The external storage stores data of programs to be respectively used by
20 the plurality of image forming apparatuses. When the external storage is connected to the connector, the data of the programs stored in the external storage are transmitted to the buffer memory through the connector and are downloaded into the respective program memories included in the controllers of the plurality of image forming apparatuses through a data transmission using the data transmission mechanism.

[0018] The data transmission mechanism may include a communications mechanism which is normally used by the
25 plurality of image forming apparatuses to exchange control data therebetween.

[0019] The data of the programs transmitted from the external storage to the buffer memory may be downloaded with the communications mechanism to the respective program memories included in the controllers of the plurality of image forming apparatuses at a transmission rate faster than a regular transmission rate used for the control data.

[0020] The communications mechanism may be a universal asynchronous receiver transmitter.

30 [0021] The data transmission mechanism may include an image data bus which is normally used by the plurality of image forming apparatuses to exchange image data therebetween.

[0022] The present application further describes a novel program downloading method. In one example, a novel program downloading method includes the steps of providing, connecting, transmitting, and downloading. The providing step provides a multi-function apparatus which includes a buffer memory, a connector, and a plurality of image forming
35 apparatuses connected each other with a data transmission mechanism. Each of the plurality of image forming apparatuses include a controller provided with a program memory for storing a program used by the image forming apparatus. The connecting step connects an external storage storing data of programs to the connector. The transmitting step transmits the data of programs stored in the external storage into the buffer memory. The downloading step downloads the data of the programs, transmitted from the external storage to the buffer memory, respectively to the program
40 memories of the controllers respectively included in the plurality of image forming apparatuses through a data transmission using the data transmission mechanism.

[0023] The data transmission mechanism may include a communications mechanism which is normally used by the plurality of image forming apparatuses to transmit control data therebetween and the downloading step uses the communications mechanism.

45 [0024] The downloading step may download the data of the programs, transmitted from the external storage to the buffer memory by the transmitting step, with the communications mechanism to the respective program memories of the controllers respectively included in the plurality of image forming apparatuses at a transmission rate faster than a regular transmission rate used for the control data.

[0025] The communications mechanism may be a universal asynchronous receiver transmitter.

50 [0026] The data transmission mechanism may include an image data bus which is normally used by the plurality of image forming apparatuses to exchange image data therebetween.

[0027] Further, the present application describes a computer readable medium storing computer instructions for performing the steps described in the above novel program downloading method.

55 [0028] A more complete appreciation of the present application and many of the attendant advantages thereof will be readily obtained as the same becomes better understood by reference to the following detailed description when considered in connection with the accompanying drawings, wherein:

Fig. 1 is a block diagram of a background digital multi-function apparatus;

Fig. 2 is a flowchart for explaining a procedure of a program downloading performed by the background digital multi-function apparatus of Fig. 1;

Fig. 3 is a block diagram of a digital multi-function apparatus according to a preferred embodiment;

Fig. 4 is a flowchart for explaining an exemplary procedure of the program downloading with a single IC card connector;

Fig. 5 is a flowchart for explaining an exemplary procedure of the program downloading using a UART configuration for a data transmission between a main controller and optional devices;

Fig. 6 is a flowchart for explaining another exemplary procedure of the program downloading using the UART configuration for the data transmission between the main controller and the optional devices; and

Fig. 7 is a flowchart for explaining an exemplary procedure of the program downloading using an application image data bus configuration for the data transmission between the main controller and the optional devices.

[0029] In describing preferred embodiments illustrated in the drawings, specific terminology is employed for the sake of clarity. However, the invention is not intended to be limited to the specific terminology so selected and it is to be understood that each specific element includes all technical equivalents which operate in a similar manner.

[0030] Referring now to the drawings, wherein like reference numeral designate identical or corresponding parts throughout the several views, particularly to Fig. 3, a digital multi-function apparatus 1 as one example of a multi-function image forming system according to an embodiment of the present application is described. Fig. 3 shows a block diagram of a main part of the digital multi-function apparatus 1 that includes a digital copying apparatus 2, as a basic machine, and optional equipment installed on the basic machine, including a printer controller 20 and a facsimile controller 30. The digital copying apparatus 2 includes a main controller 10 which is provided with a CPU (central processing unit) 11 that controls the entire operations of the digital multi-function apparatus 1.

[0031] The digital multi-function apparatus 1 performs a copying operation in which image data output from a reading controller 40 after an original is read by a reading unit (not shown) is sent to a writing controller 50 via an image controller 16. The writing controller 50 controls an LD (laser diode), not shown, to turn on and off according to the image data and forms an image on a transfer sheet based on a known electrophotographic procedure (not shown).

[0032] Each of the printer controller 20 and the facsimile controller 30 is connected to the digital copying apparatus 2 with a UART (Universal Asynchronous Receiver Transmitter) serving as a data transmitting configuration and an application image data bus serving as a data bus for sending data of an application image.

[0033] During a print operation, the printer controller 20 receives print data sent through a network with a network interface mechanism (not shown) and sent a print image generated by an image controller 26 based on the print data to the digital copying apparatus 2 so as to perform an image forming operation. This operation is performed by an execution of a printer application program by a CPU (central processing unit) 21 included in the printer controller 20.

[0034] During a facsimile operation, the facsimile controller 30 transmits a facsimile image, generated and sent to the facsimile controller 30 by the digital copying apparatus 2, to a facsimile line with a facsimile line interface (not shown), or receives a facsimile signal from the facsimile line via the facsimile line interface and sends it to the digital copying apparatus 2 so as to perform the image forming operation. This operation is performed by an execution of a facsimile application program by a CPU (central processing unit) 31 included in the facsimile controller 30.

[0035] In the above operations, the digital copying apparatus 2 exchanges with the printer controller 20 and the facsimile controller 30 control data through UARTs (Universal Asynchronous Receiver Transmitter) 14, 24, and 34 and image data through the application image data bus connecting image controllers 16, 26, and 36 each other so that the digital multi-function apparatus 1 can perform a sequential operation under a multi-function operating system.

[0036] The main controller 10 is provided with a ROM (read only memory) 12 that stores a plurality of programs including a program used by the CPU 11 which governs the entire operations of the digital multi-function apparatus 1. To update the programs, an IC (Integrated circuit) card 17, serving as an external storage device and which stores a latest program, is connected to an IC (Integrated circuit) card connector 15 and the program is downloaded to the ROM 12 via a CPU bus.

[0037] Likewise, the printer controller 20 and the facsimile controller 30 are provided with ROMs (read only memories) 22 and 32, respectively, which govern the operations of the respective units. To update the programs for these optional devices, the IC card 17 which also stores latest programs for the optional devices is connected to the IC card connector 15 and the respective programs are downloaded to the ROM 22 and 32 via the CPU bus and the UARTs.

[0038] Thus, downloading of the programs to the respective memory devices can be performed through a single common connector, the IC card connector 15. Thereby, a number of connectors can be minimized.

[0039] Referring to Fig. 4, an operation for downloading programs to the digital copying apparatus 2 and the optional devices through the IC card connector 15 is explained.

After the IC card 17 is inserted into the IC card connector 15 in Step S201, the digital multi-function apparatus 1 is given power in Step S202 and then a download program stored in the IC card 17 is initiated in Step S203.

[0040] In Step S204, one of the optional devices is selected so that the program is downloaded to the device selected.

An instruction for this selection may be provided inside the IC card 17 or through an operation panel 60 of the digital copying apparatus 2.

[0041] When the digital copying apparatus 2 is selected as the device for the downloading in Step S204, the process proceeds to Step S205 and the contents of the IC card 17 is sequentially transmitted to the ROM 12 via the CPU bus in Step S206. Whether the transmission of the program is completed is checked in Step S207. If the transmission is determined as completed in Step S207, a process for completing the program downloading is performed in Step S216. Then, the process ends.

[0042] When the facsimile controller 30 is selected as the device for downloading in Step S204, the process proceeds to Step S208. Then, in Step S209, the contents of the IC card 17 is transmitted to and is temporarily stored in a storage of the main controller 10, including a RAM (random access memory) 13, via the CPU bus and the program temporarily stored is transmitted to a memory for temporarily storing facsimile transmission data held within the facsimile controller 30 via a data transmission configuration connecting the main controller 10 and the facsimile controller 30 each other. Whether the transmission of the program is completed is checked in Step S210. If the transmission is determined as completed in Step S210, the program temporarily stored in the memory within the facsimile controller 30 is downloaded to a ROM (read only memory) 32 in Step S211 and a process for completing the program downloading is performed in Step S216. Then, the process ends.

[0043] Likewise, when the printer controller 20 is selected as the device for downloading in Step S204, the process proceeds to Step S212. Then, in Step S213, the contents of the IC card 17 is transmitted to and is temporarily stored in a storage of the main controller 10, including the RAM (random access memory) 13, via the CPU bus and the program temporarily stored is transmitted to a memory for temporarily storing printer transmission data held within the printer controller 20 via a data transmission configuration connecting the main controller 10 and the printer controller 20 each other. Whether the transmission of the program is completed is checked in Step S214. If the transmission is determined as completed in Step S214, the program temporarily stored in the memory within the printer controller 20 is downloaded to a ROM (read only memory) 22 in Step S215 and a process for completing the program downloading is performed in Step S216. Then, the process ends.

[0044] In the above procedure of Fig. 4, the program downloading to the optional device is performed through the data transmission configuration that connects the main controller 10 and either the printer controller 20 or the facsimile controller 30 each other. Such data transmission corresponds to the processes performed in Steps S209 and S210 or Steps S213 and S214. Fig. 5 shows another flowchart in which the data transmission performed in Steps S209 and S210 or Steps S213 and S214 are performed with the UART configuration. It is assumed that, before starting the process of Fig. 5, several operations are performed; the IC card 17 is inserted into the IC card connector 15, the program data is transmitted from the IC card 17, and the program data is stored in the memory of the main controller 10, including the RAM 13.

[0045] In Step S301 in Fig. 5, the transmission of the program data to the facsimile controller 30 or the printer controller 20 is started. In Step S302, the program data representing the contents of the IC card 17 is written into a transmission register of the UART 14. In Step S303, the program data written in the register of the UART 14 is sequentially transmitted to the corresponding option device through the UART interface. In Step S304, whether transmission of a designated amount of data is completed is determined by checking the transmitted data between the UARTs. If the transmission is determined as completed in Step S304, a process for terminating the transmission process is performed in Step S305. Then, the process ends.

[0046] Generally, exchanges of the control data between the main controller 10 and the printer controller 20 and between the main controller 10 and the facsimile controller 30 are performed with the UART. The transfer rate for the control data transmission with the UART is typically set to a value in a range of from 4,800 bps to 14,400 bps.

[0047] In the present embodiment, each of the programs used for the main controller 10, the printer controller 20, and the facsimile controller 30 and which are subjected to be the downloading, typically has a volume of 1 to 2 Mbytes although it depends upon a scale of the system.

[0048] As an example, the following case is examined, in which the UART has the conditions of the following 5 items and each of the control programs for the printer controller 20 and the facsimile controller 30 has a volume of 2 Mbytes.

1. Baud rate is 9,600 bps.
2. Start bit is 1 bit.
3. Data bit is 8 bits.
4. Parity bit is odd, 1 bit.
5. Stop bit is 1 bit.

[0049] In the above case, transmission of 1-byte data requires a time of approximately 1.146 ms which calculation is made as follows;

$$\text{number of bits} \times \text{baud rate} = (1 + 8 + 1 + 1) \times 1/9600 \square 1.146 \text{ ms.}$$

[0050] If the transmission is successively executed without no time consumption between data blocks, the transmission of 2-Mbyte data requires a time of approximately 38.2 min which is calculated as follows;

$$1.146 \text{ ms/byte} \times 2 \text{ Mbyte} \square 2292 \text{ ms} \square 38.2 \text{ min.}$$

[0051] This time period may be too long and cannot be a realistic figure for the downloading.

[0052] To make it faster, the UART is set to a baud rate faster than the rate normally used for transmitting the control data. As an example, a baud rate of 16 times faster is examined, that is, 9,600 bps is increased to 153.600 bps.

[0053] In this case, transmission of 2-byte data requires a time of approximately 2.4 min., which can be considered as realistic as the downloading. The time is calculated as follows;

$$(1 + 8 + 1 + 1) \times 1/153600 \text{ ms/byte} \times 2 \text{ Mbyte} \square 143.2 \text{ s}$$

$$\square 2.4 \text{ s.}$$

[0054] Referring to Fig. 6, an exemplary procedure of the program downloading with a faster UART baud rate is explained. In the program downloading shown in Fig. 6, the downloading of the program is set to a baud rate faster than the one normally used for transmitting the control data with the UART configuration between the main controller 10 and the optional devices (i.e., the printer controller 20 and the facsimile controller 30). Fig. 6 shows the procedure of data transmission between the main controller 10 and the optional devices, which corresponds to Steps S209 and S210 or Steps S213 and S214 of Fig. 4. It is assumed that, before starting the process of Fig. 6, several operations are performed; the IC card 17 is inserted into the IC card connector 15, the program data is transmitted from the IC card 17, and the program data is stored in the memory of the main controller 10, including the RAM 13.

[0055] In Step S401 of Fig. 6, the transmission of the program data to the facsimile controller 30 or the printer controller 20 is started. Initially, in Step S402, the UART 14 is set to a baud rate faster than the regular baud rate at which, as regular operations other than the program downloading, the control data is transmitted to the optional devices so as to activate the optional devices. In Step S403, the program data representing the contents of the IC card 17 is written into the transmission register of the UART 14. In Step S404, the program data written in the register of the UART 14 is sequentially transmitted to the corresponding option device through the UART interface. In Step S405, whether transmission of a designated amount of data is completed is determined by checking the transmitted data between the UARTs. If the transmission is determined as completed in Step S405, a process for terminating the transmission process is performed in Step S406. Then, the process ends.

[0056] Referring now to Fig. 7, an exemplary procedure of the program downloading through the application image data bus is explained. In the program downloading shown in Fig. 7, the downloading of the program is performed with the application image data bus configuration between the main controller 10 and the optional devices (i.e., the printer controller 20 and the facsimile controller 30), which steps correspond to Steps S209 and S210 or Steps S213 and S214 of Fig. 4. It is assumed that, before starting the process of Fig. 7, several operations are performed; the IC card 17 is inserted into the IC card connector 15, the program data is transmitted from the IC card 17, and the program data is stored in the memory of the main controller 10, including the RAM 13.

[0057] In Step S501 of Fig. 7, the transmission of the program to the facsimile controller 30 or the printer controller 20 is started. Initially, in Step S502, the program data stored in the memory of the main controller 10 (i.e., the RAM 13) is written into the image controller 16 using, for example, a transmission buffer included in the image controller 16 so as to transmit the program data through the application image data bus. In Step S503, the program data written in the image controller 16 is sequentially transmitted to the corresponding option device through the application image data bus using, for example, the transmission buffer of the image controller 16. In Step S504, whether transmission of a designated amount of data is completed is determined by checking the transmitted data between the transmission and receiving buffers of the image controllers 16 and 26 and between the transmission and receiving buffers of the image controllers 16 and 36. If the transmission is determined as completed in Step S504, a process for terminating the transmission process is performed in Step S505. Then, the process ends.

[0058] In this way, the program downloading is performed through the application image data bus between the main controller 10 of the digital copying apparatus 2 and the optional devices (i.e., the printer controller 20 and the facsimile controller 30). Thereby, the digital multi-function apparatus 1 can perform the program downloading in a relatively faster manner.

[0059] The present embodiment is provided with a download program that has predicates of an execution procedure for downloading, from an external storage, programs used as control programs by the CPU 11 of the main controller 10, the image controller 16, the CPU 21 of the printer controller 20, and the CPU 31 of the facsimile controller 30. By initiating this download program, the digital multi-function apparatus 1 can perform the program downloading relative to a focused program. This download program can be recorded in any kind of machine readable medium (i.e., a semiconductor memory, a floppy disk, etc.) and, therefore, it can be prestored in the ROM 12, 22, or 32 under the control of the CPU 11 and the main controller 16, the CPU 21, or the CPU 31. It may also be possible that the download program is stored in a medium, the program is installed in the RAM 13, 23, or 33, and the download program installed in the RAM 13, 23, or 33 is then initiated to execute the download program.

[0060] This invention may be conveniently implemented using a conventional general purpose digital computer programmed according to the teaching of the present specification, as will be apparent to those skilled in the computer art. Appropriate software coding can readily be prepared by skilled programmers based on the teachings of the present disclosure, as will be apparent to those skilled in the software art. The present invention may also be implemented by the preparation of application specific integrated circuits or by interconnecting an appropriate network of conventional component circuits, as will be readily apparent to those skilled in the art.

[0061] Numerous additional modifications and variations of the present application are possible in light of the above teachings. It is therefore to be understood that within the scope of the appended claims, the present application may be practiced otherwise than as specifically described herein.

[0062] This application claims priority to Japanese patent application No. JPAP2000-132712 filed on May 1, 2000 in the

Japanese Patent Office, the entire contents of which are hereby incorporated by reference.

Claims

1. A multi-function apparatus, comprising:

a plurality of image forming apparatuses, each comprising a controller including a program memory for storing a program used by the image forming apparatus;
a buffer memory;
a data transmission mechanism configured to connect said plurality of image forming apparatuses to each other so as to allow a data transmission therebetween; and
a connector configured to connect an external storage to said data transmission mechanism so as to allow a data transmission from said external storage to said buffer memory, said external storage storing data of programs to be respectively used by said plurality of image forming apparatuses,
wherein, when said external storage is connected to said connector, said data of at least one of said programs stored in said external storage are transmitted to said buffer memory through said connector and are downloaded into said respective program memories included in said controllers of said plurality of image forming apparatuses through a data transmission using said data transmission mechanism.

2. A multi-function apparatus as defined in Claim 1, wherein said data transmission mechanism includes a communications mechanism which is normally used by said plurality of image forming apparatuses to exchange control data therebetween.

3. A multi-function apparatus as defined in Claim 2, wherein said data of said programs transmitted from said external storage to said buffer memory are downloaded with said communications mechanism to said respective program memories included in said controllers of said plurality of image forming apparatuses at a transmission rate faster than a regular transmission rate used for said control data.

4. A multi-function apparatus as defined in Claim 2 or 3, wherein said communications mechanism is a universal asynchronous receiver transmitter.

5. A multi-function apparatus as defined in Claim 1, wherein said data transmission mechanism includes an image data bus which is normally used by said plurality of image forming apparatuses to exchange image data therebetween.

6. A multi-function apparatus, comprising:

a plurality of image forming apparatuses, each comprising controlling means including program memory means for storing a program used by the image forming apparatus;

buffer memory means;

data transmission means for connecting said plurality of image forming apparatuses each other so as to allow a data transmission therebetween; and

connecting means for connecting an external storage to said data transmission means so as to allow a data transmission from said external storage to said buffer memory means, said external storage storing data of programs to be respectively used by said plurality of image forming apparatuses,

wherein, when said external storage is connected to said connecting means, said data of at least one of said programs stored in said external storage are transmitted to said buffer memory means through said connecting means and are downloaded into said respective program memory means included in said controlling means of said plurality of image forming apparatuses through a data transmission using said data transmission mechanism.

7. A multi-function apparatus as defined in Claim 6, wherein said data transmission mechanism includes a communications mechanism which is normally used by said plurality of image forming apparatuses to exchange control data therebetween.

8. A multi-function apparatus as defined in Claim 7, wherein said data of said programs transmitted from said external storage to said buffer memory means are downloaded with said communications mechanism to said respective program memory means included in said controlling means of said plurality of image forming apparatuses at a transmission rate faster than a regular transmission rate used for said control data.

9. A multi-function apparatus as defined in Claim 7 or 8, wherein said communications mechanism is a universal asynchronous receiver transmitter.

10. A multi-function apparatus as defined in Claim 6, wherein said data transmission mechanism includes an image data bus which is normally used by said plurality of image forming apparatuses to exchange image data therebetween.

11. A method for program downloading, comprising the steps of:

providing a multi-function apparatus comprising:

a buffer memory;

a connector; and

a plurality of image forming apparatuses connected each other with a data transmission mechanism, each of said plurality of image forming apparatuses including a controller provided with a program memory for storing a program used by the image forming apparatus;

connecting an external storage storing data of programs to said connector;

transmitting said data of programs stored in said external storage into said buffer memory; and

downloading said data of said programs, transmitted from said external storage to said buffer memory, respectively to said program memories of said controllers respectively included in said plurality of image forming apparatuses through a data transmission using said data transmission mechanism.

12. A method as defined in Claim 11, wherein said data transmission mechanism includes a communications mechanism which is normally used by said plurality of image forming apparatuses to transmit control data therebetween and said downloading step uses said communications mechanism.

13. A method as defined in Claim 12, wherein said downloading step downloads said data of said programs, transmitted from said external storage to said buffer memory by said transmitting step, with said communications mechanism to said respective program memories of said controllers respectively included in said plurality of image forming apparatuses at a transmission rate faster than a regular transmission rate used for said control data.

14. A method as defined in Claim 12 or 13, wherein said communications mechanism is a universal asynchronous receiver transmitter.

15. A method as defined in Claim 11, wherein said data transmission mechanism includes an image data bus which is normally used by said plurality of image forming apparatuses to exchange image data therebetween.

16. A computer readable medium storing computer instructions for performing the steps recited in anyone of Claims 11 - 15.

17. A multi-function image processing apparatus, comprising:

a plurality of image forming apparatuses, each comprising a controller including a program memory for storing a program used by the image forming apparatus;
a buffer memory;
a data transmission mechanism configured to connect said plurality of image forming apparatuses each other so as to allow a data transmission therebetween; and
a connector configured to connect an external storage to said data transmission mechanism so as to allow a data transmission from said external storage to said buffer memory, said external storage storing data of programs to be respectively used by said plurality of image forming apparatuses,
wherein, when said external storage is connected to said connector, said data of said programs stored in said external storage are transmitted to said buffer memory through said connector and are downloaded into said respective program memories included in said controllers of said plurality of image forming apparatuses through a data transmission using said data transmission mechanism.

18. An apparatus as defined in Claim 17, wherein said plurality of image forming apparatuses includes at least a copying apparatus, a printing apparatus, and a facsimile apparatus.

FIG. 1

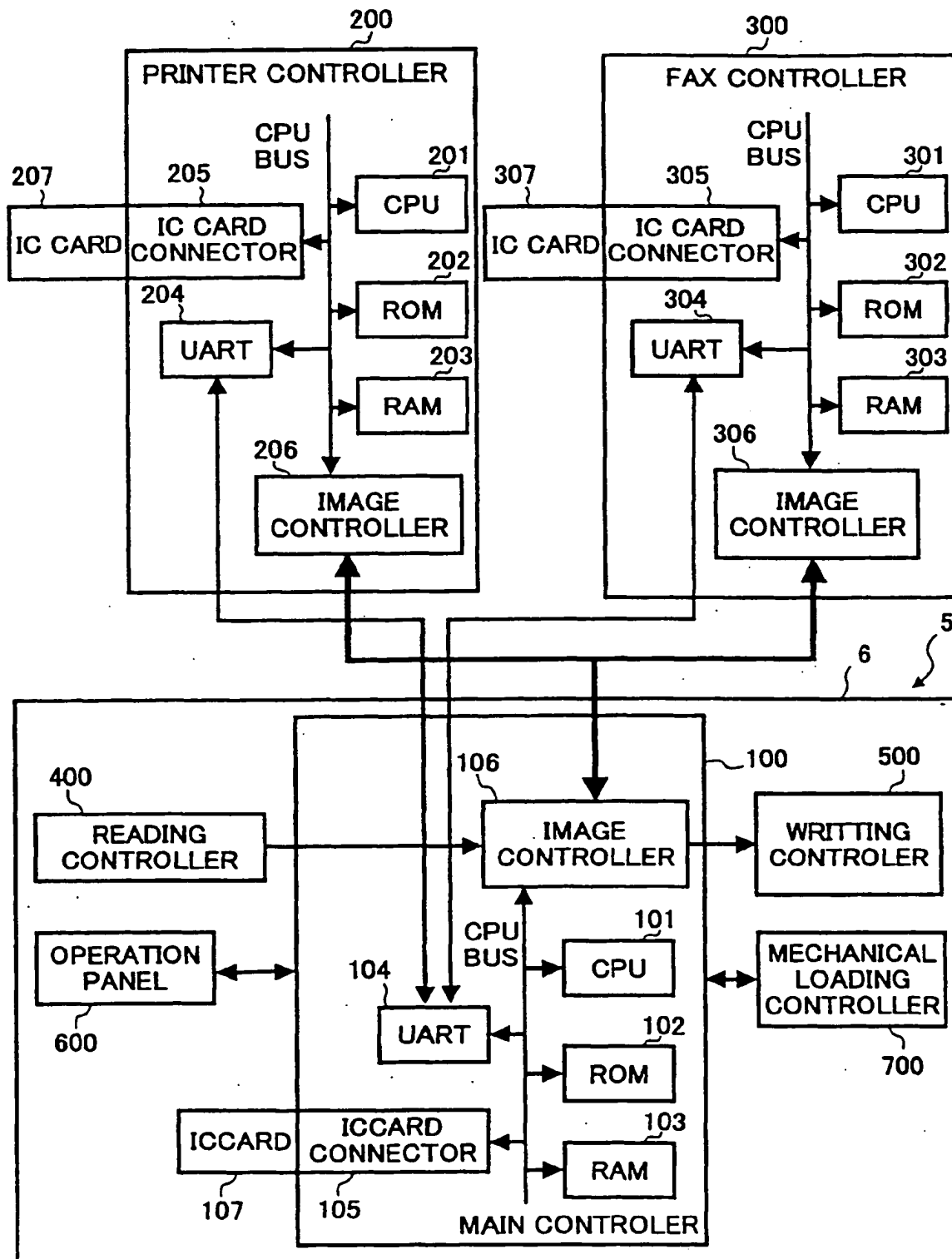


FIG. 2

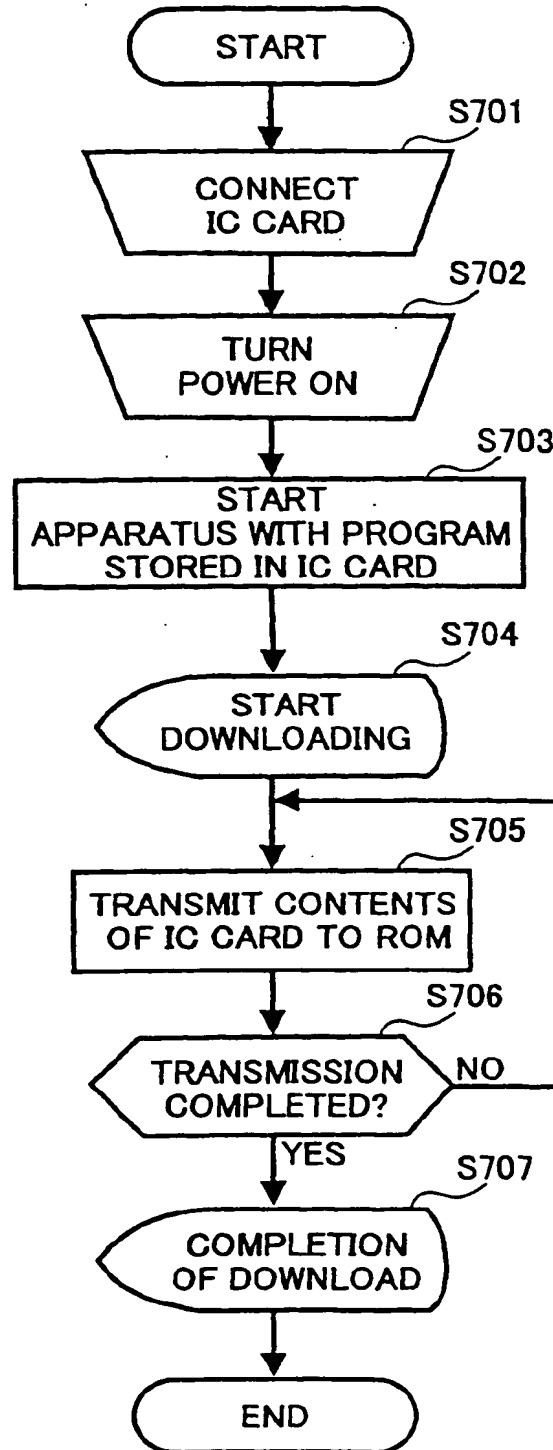


FIG. 3

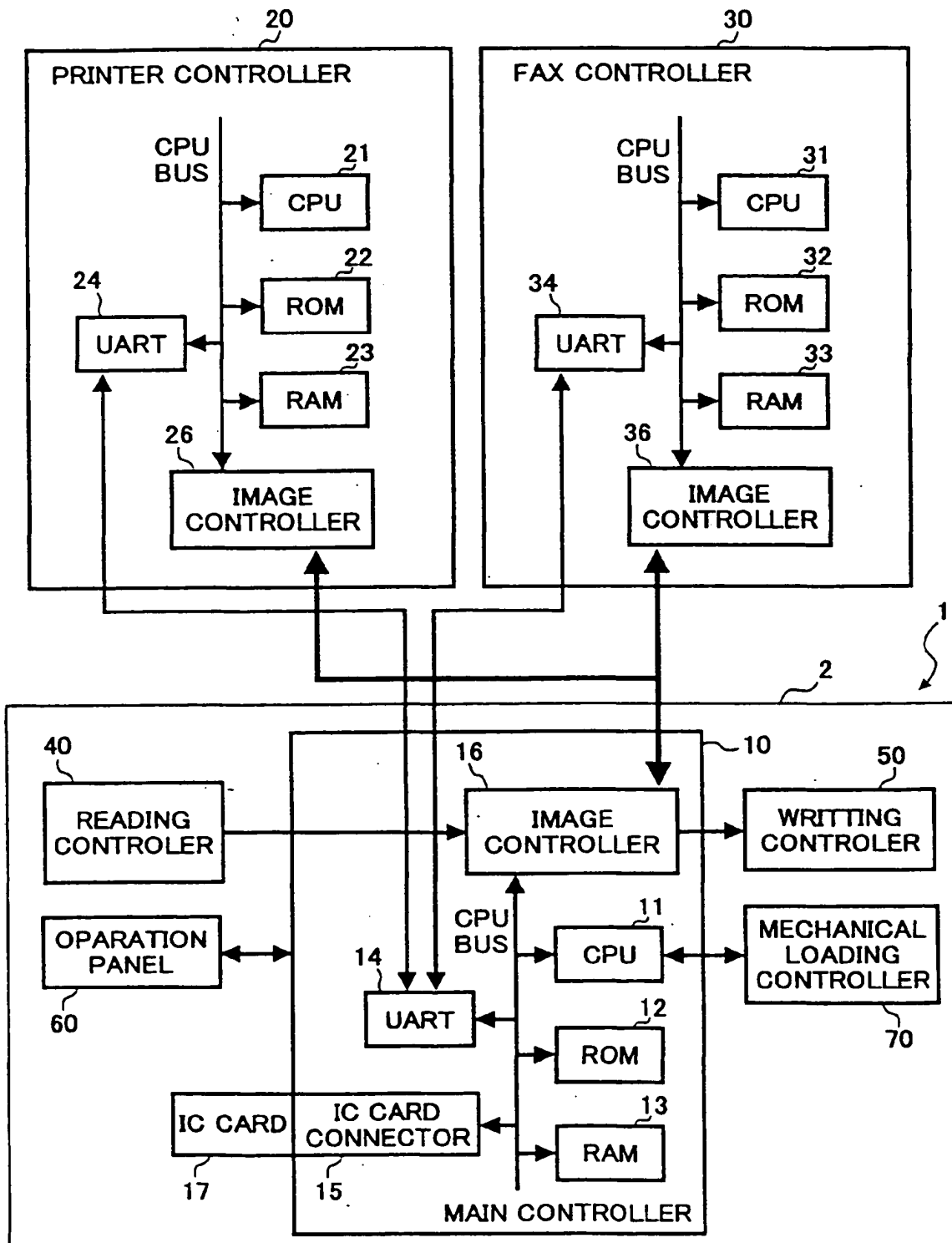


FIG. 4

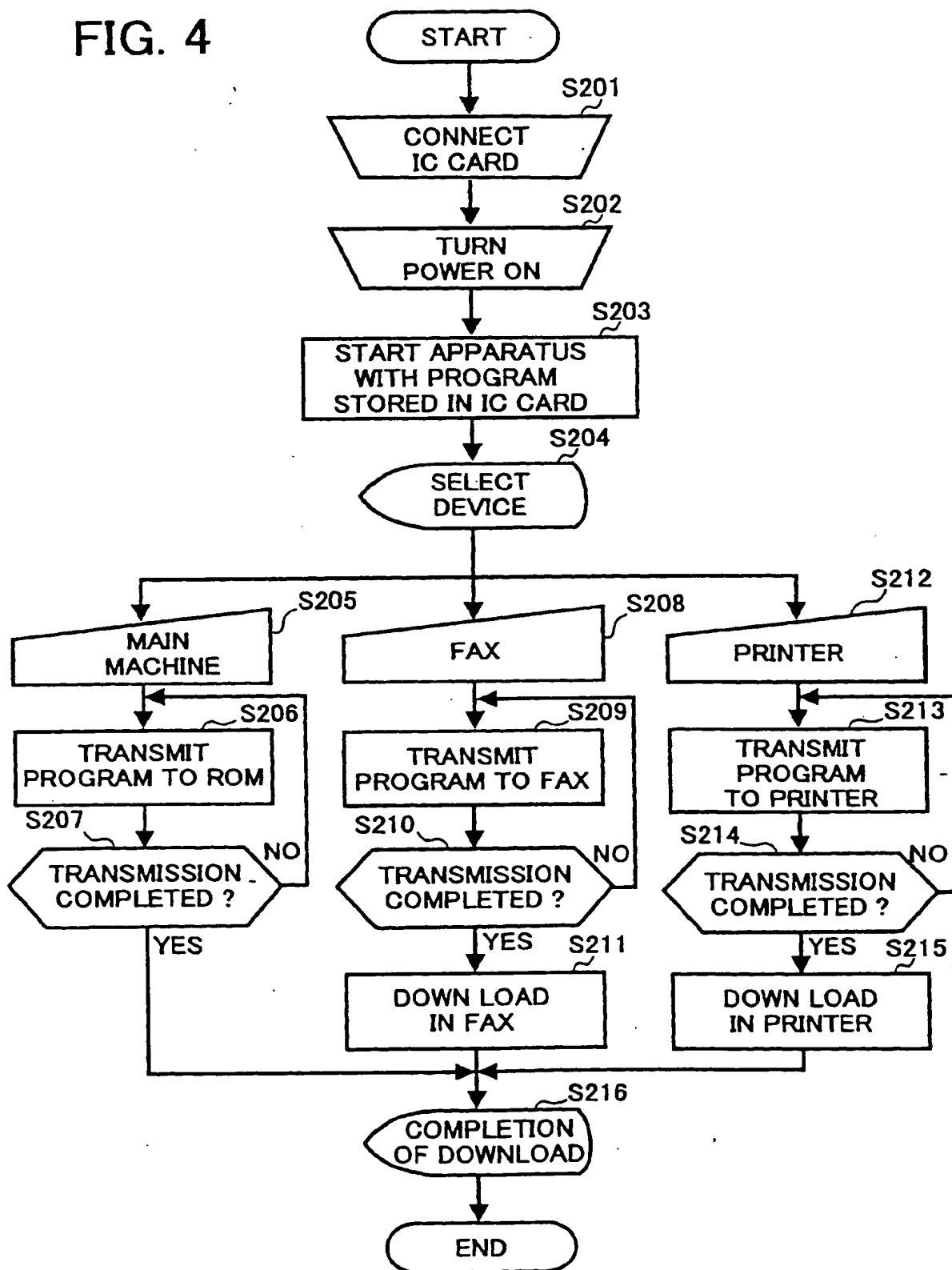


FIG. 5

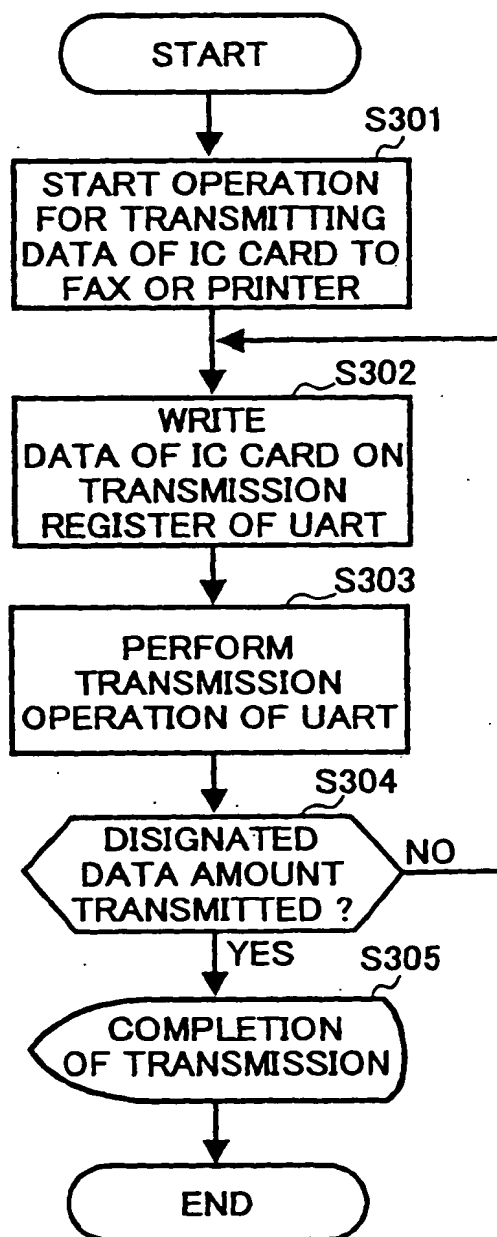


FIG. 6

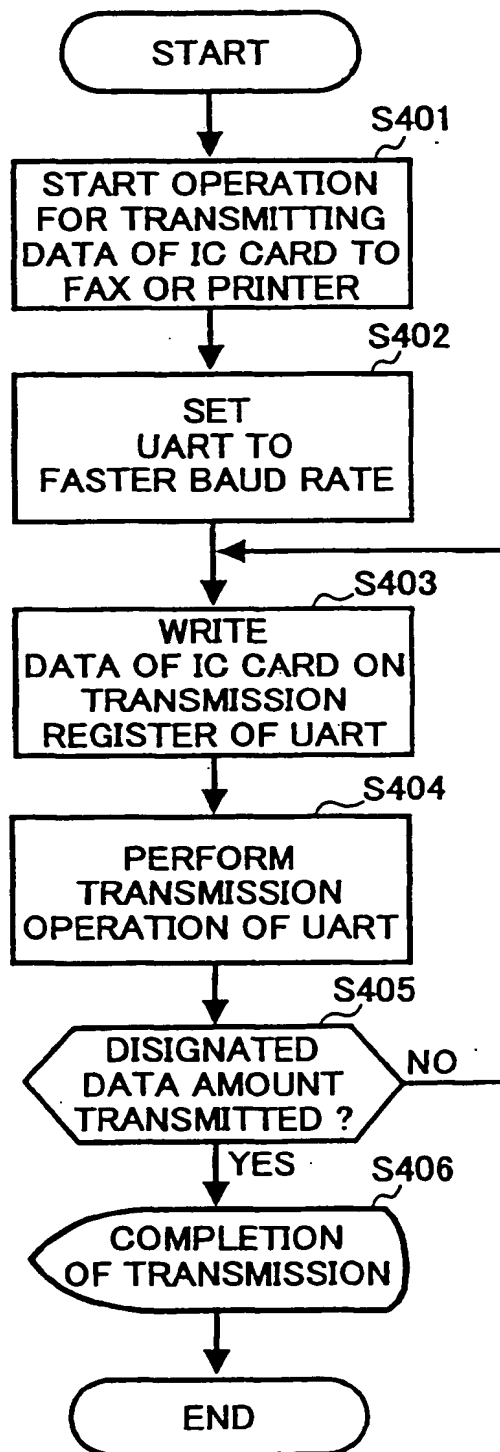


FIG. 7

